

XIX

Nineteenth Lecture
AUTOMATA & FORMAL LANGUAGES
Friday, 17 November 2023

We are in the proof of

Theorem Every recursive function is computable.
[The converse is also true; discussed later.]

Lecture XVIII, page 8.

By induction: show closure of computable functions
under RECURSION & MINIMISATION.

Recursion
Lecture XVIII
page 9

Recursion Fix f, g computable

$$h(\vec{w}, \epsilon) = f(\vec{w})$$

$$h(\vec{w}, s(v)) = g(\vec{w}, v, k(\vec{w}, v))$$

Show k is computable.

Fix \vec{w}, v & describe a RM that computes $k(\vec{w}, v)$.

Two scratch registers k & l .

Compute $f(\vec{w})$ and write into register l .

Check if $v = \epsilon$. $\xrightarrow{\text{YES}}$ Output register l .

\downarrow NO

REPEAT SUBROUTINE

UNTIL v is equal to register k .

SUBROUTINE

Compute $g(\vec{w}, v_0, v_1)$
where v_0 is the content of reg. k
& v_1 is the content of reg. l

Write result into register l .

Apply s to register k .

Output register l .

Minimisation

Suppose $f : W^{k+1} \dashrightarrow W$ is a partial function, then the partial function h defined by

$$h(\vec{w}) := \begin{cases} v & \text{if for all } u \leq v, \text{ we have that } f(\vec{w}, u) \downarrow \text{ and} \\ & v \text{ is } < \text{-minimal such that } f(\vec{w}, v) = \varepsilon \text{ or} \\ \uparrow & \text{otherwise} \end{cases}$$

is called the *minimisation result* of f .

Proof that if f is computable, then so is h .
Input \vec{w} . Describe the RM that performs $h(\vec{w})$.

Use scratch registers k, l, m , all initially empty.

Repeat **SUBROUTINE**
until register l is empty.
Write reg. k as output.

SUBROUTINE

- Replace content of reg. k with content of reg. m .
- Apply s to reg. k and write the result in reg. m .
- Compute $f(\vec{w}, u)$ where u is reg. k .
- Write result in reg. l .

q.e.d.

Applications

Already saw that $+$, \cdot are recursive.

Now we know they are computable.

We mean $(v, w) \mapsto v$ iff $\#v = \#v + \#w$.

In particular, consider

$$z: (i, j) \mapsto \frac{(i+j)(i+j+1)}{2} + j.$$

the **CANTOR ZIGZAG FUNCTION**.

This gives us the computability of the function

$$(v, w) \mapsto v \text{ iff } \#v = z(\#v, \#w)$$

which is a bijection between \mathbb{N}^2 and \mathbb{N} .

This allows us to computably merge and split words.



$$(v, w) \mapsto v \quad \text{iff} \quad \#v = z(\#v, \#w)$$

Write $v =: v * w$

MERGING

Similarly, the maps

$$v \mapsto v_{(0)}$$

$$v \mapsto v_{(1)}$$

with property that $v_{(0)} * v_{(1)} = v$
are computable.

SPLITTING

→ ES #3
Example (40)

§ 4.7 UNIVERSALITY

Machines used to be built for one purpose;
modern computers can change their programme
[the difference between **HARDWARE** &
SOFTWARE].

This is a fundamental cultural change.

Coding of machines

Using § 4.6, we add extra symbols to
make things easy for us.

0 1 () , ⊕ = ? ⇌ □ ε

↑ ↑
boldface
parenthesis

↑
boldface comma

Natural numbers

Encode them in binary:

0	0
1	1
10	2
11	3

etc.

code(k) for
the code of k

States

Since Q does not matter as a set,
we can assume that

$$Q = \{q_0, \dots, q_k\} \quad \text{for some fixed listing of states}$$

$$\text{code}(q_i) := \text{code}(i).$$

Instructions

$$\text{Example: } I = +(k, a, q')$$

$$\text{code}(I) :=$$

$$\#(\text{code}(k), a, \text{code}(q'))$$

Similarly for the other instructions.

Programs

$$q \mapsto P(q) \quad \text{where } P(q) \text{ is an instruction}$$

$$\text{code}(q \mapsto P(q)) := \text{code}(q) \# \text{code}(P(q)).$$

Σ -RM

$$(\Sigma, Q, P) = M \quad \text{where } Q = \{q_0, \dots, q_k\}$$

$$\text{code}(M) := \text{code}(q_0 \mapsto P(q_0)) \triangleright \dots \triangleright \text{code}(q_k \mapsto P(q_k))$$

Seq. of words in $W = \Sigma^*$

$$\vec{w} = (w_0, \dots, w_u) \quad \text{code}(\vec{w}) := w_0 \square \dots \square w_u.$$

Configurations

$$C = (q, \vec{w}) \quad \text{code}(C) := \text{code}(q) \square \text{code}(\vec{w})$$

Observation (1) Whether something is a code for

- state
- instruction
- program line
- RM
- seq. of word
- config.

can be checked by a RM.

(2) If C is a configuration and M a reg. machine with $w = \text{code}(C)$ and $v = \text{code}(M)$, then

$$(w, v) \mapsto v$$

where $v = \text{code}(C')$ and M transforms C to C'

is performed by a RM.

Proposition

The operation

$$w, v, u \mapsto \text{code}(C(M, \vec{w}, \#u))$$

where $\text{code}(M) = w$, $\text{code}(\vec{w}) = v$

can be performed by a RM.

Proof. $C(M, \vec{w}, u)$ was defined in terms of the TRANS-FORM operation by recursion. Using Obs. (2) & the fact that comp. fns are closed under recursion yield the desired result. q.e.d.

Corollary 4.25. The total operation "check whether M has halted with input \vec{w} after at most $\#v$ steps" can be performed by a register machine. We call the corresponding total (characteristic) function

$$t_{M,k}(\vec{w}, v) := \begin{cases} a & M \text{ has halted with input } \vec{w} \text{ after at most } \#v \text{ steps and} \\ \varepsilon & \text{otherwise} \end{cases}$$

the truncated computation of M .

Note that this is a decursive function.

Proof.

We construct RM with scratch registers k, l .

Repeat SUBROUTINE
until register $k = v$.
Halt and output ε .

Note that we can only call the subroutine $\#v$ many times.

Since $(\mathbb{N}, \leftarrow) \cong (\mathbb{N}, \rightarrow)$, this parameter is finite.

SUBROUTINE Replace content of reg. k by content of reg. l

Compute $(CM, \vec{w}, v) = (q, \vec{v})$
where v is the content of k

Check if $q = q_H$

YES

Halt & output a .

NO

Apply s to reg. k and write the result in reg. l .

q.e.d.

Theorem 4.26 (The Software Principle). There is a register machine U , called a *universal register machine* such that for every register machine M and sequence of words \vec{w} , we have that

$$f_{U,2}(v, u) = \begin{cases} f_{M,k}(\vec{w}) & \text{if } v = \text{code}(M) \text{ for a register machine } M \\ & \text{and } u = \text{code}(\vec{w}) \text{ for a sequence of words of length } k, \\ \uparrow & \text{otherwise.} \end{cases}$$

Proof in Lecture XX

The Software Principle changes the paradigm of "machine".

Traditionally, we had one machine per task, now we have one machine U

[for **UNIVERSAL REGISTER MACHINE**]

that can do the job of any machine with the right input !!!

Remark. U has a fixed upper register index and this means that a machine with limited memory can do ANY task, assuming that you feed it the right software [essentially, the code of the machine performing the task].