Parametric Model Order Reduction in MEMS and IC Design

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At our institute, model order reduction (MOR) methods have been used with great success in model generation and simulation of integrated circuits as well as heterogeneous systems such as micro electrical mechanical systems (MEMS), see [1], [6], [7], [7] for reference. In general, we use tools like ANSYS or Simlab’s PCBMod to obtain linear time-invariant descriptor systems of the form:

\[
\begin{align*}
M \ddot{q} + D \dot{q} + K q &= B^0 \text{in} u \\
y &= B^{\text{int}}_1 q + B^{\text{out}}_2 \dot{q} + D \text{Du}
\end{align*}
\]

\[ (1) \]

\[ M, D, K \in \mathbb{R}^{N \times N}; B^\text{in} \in \mathbb{R}^{N \times p}; B^{\text{int}}_1, B^{\text{out}}_2 \in \mathbb{R}^{q \times N}; D \in \mathbb{R}^{q \times p} \] are sparse, but with the state space dimension \( N \) reaching magnitudes of \( 10^5 \ldots 10^7 \), time domain simulations become inefficient, especially when (1) is only part of a bigger system obtained by coupling.

During the last decades, Krylov subspace based MOR schemes proved as a practical way to efficiently reduce the state space dimension ([2], [4]). But we lately face a growing demand for an extension of these methods to parameter dependent models. Thus, Fraunhofer started CAROD (Computer-Aided Robust Design) [3], a joint research project for tools incorporating manufacturing tolerances during simulation and design.

As the tools for the generation of the system matrices of (1) behave like black boxes, the analytical relationship between parameter values and resulting system matrices are unknown. Thus, in our terms parametric model order reduction (PMOR) involves two aspects:

1. Derivation of a parameter dependent full order system from a series of systems of form (1) related to a series of fixed parameter values
2. Reduction of the state space dimension of the parameterized system while keeping the parameter dependency of the system matrices

The talk will outline the state of the art of parametric model order reduction at Fraunhofer IIS/EAS including real world examples, pitfalls and open problems.